Serial No. 09/751,659 Qocket No.: 55419 (70158)

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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:	)	,
MARTIN, Brian James Martin	)	Group Art Unit: 2195
Serial No.: 09/751,659	)	Examiner: Tang, Kenneth
Filed: December 30, 2000	)	

For: PARALLEL DISPATCH WAIT SIGNALING METHOD, METHOD FOR REDUCING CONTENTION OF HIGHLY CONTENDED DISPATCHER LOCK, AND RELATED OPERATING SYSTEMS, MULTIPROCESSOR COMPUTER SYSTEMS AND PRODUCTS

## **MAIL STOP AF**

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

## **Amendment in Response to Final Office Action**

Sir:

In response to the *final* Office Action dated June 16, 2005, please amend the above-identified application as follows:

**Amendments to the Claims** are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 19 of this paper.

CERTIFICATE OF EXPRESS MAIL

I hereby certify that this paper (along with any paper referred to as being attached or enclosed is being deposited with the United States Postal Service in an envelope as "Express Mail Post Office to Addressee," Express Mail Label No. **EV438976374US** addressed to: **MAIL STOP AF**, Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450 on

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Donna R. Davis